

## II. AMENDMENTS TO THE SPECIFICATION:

On page 9, par. 0042 starting at line 3, please amend as follows:

The output 30 from the signal generator 2 is ~~[[used]]~~ connected to first 46 and second 48 power transistors, in this case MOSFETs. One of the transistors is driven directly from output node 30, whereas the other transistor is driven through inverter 51. The transistors 46, 48 are connected in series between the input high 40 and low 42 dc voltage terminals. The node joining the two transistors is known as the drive node since the output drive is taken from this point.